

AMENDMENTS TO THE CLAIMS

1. (Previously Presented) A liquid crystal display (LCD) panel, comprising:
 - a plurality of data lines included in a display area of the LCD panel;
 - a plurality of gate lines crossing the data lines, the data lines and the gate lines defining a plurality of pixels;
 - a dummy data line included in a non-display area outside the display area and formed in parallel to the data lines, to allow each pixel to have substantially a same parasitic capacitance;
 - a plurality of switching devices positioned at intersections between the data lines and the gate lines; and
 - a plurality of pixel electrodes each driven by one of the switching devices.
2. (Original) The liquid crystal display panel as claimed in claim 1, wherein the dummy data line is supplied with a signal having an inverted phase with respect to data on a one of the data lines adjacent to the dummy data line.
3. (Original) The liquid crystal display panel as claimed in claim 1, further comprising:
 - a plurality of dummy switching devices positioned intersections between the dummy data line and the gate lines; and
 - a plurality of dummy pixel electrodes each connected to one of the dummy switching devices.
4. (Original) The liquid crystal display panel as claimed in claim 3, wherein the dummy data line and the dummy pixel electrodes each further include a black matrix for blocking light.
5. (Original) The liquid crystal display panel as claimed in claim 2, further comprising:
 - dummy voltage supply means for supplying the signal to the dummy data line.
6. (Original) The liquid crystal display panel as claimed in claim 5, wherein the dummy voltage supply means includes an inverter for performing a phase inversion of a signal on a data line adjacent to the dummy data line.
7. (Original) The liquid crystal display panel as claimed in claim 5, wherein the dummy voltage supply means includes a shorting bar for electrically connecting the dummy data line to

the data line supplied with a voltage having the same phase as a voltage applied to the dummy data line.

8. (Original) The liquid crystal display panel as claimed in claim 5, wherein the dummy voltage supply means includes a dummy voltage generator for directly applying a voltage to the dummy data line.

9. (Previously Presented) A liquid crystal display (LCD) panel, comprising:
a plurality of data lines included in a display area of the LCD panel;
a plurality of gate lines crossing the data lines, the data lines and the gate lines defining a plurality of pixels;
a plurality of switching devices positioned at intersections between the data lines and the gate lines;
a plurality of pixel electrodes each supplied with a voltage on the data line by a switching of the switching device; and
a dummy data line for compensating a capacitor value difference of an adjacent pixel electrode thereto to allow each pixel to have substantially a same parasitic capacitance.

10. (Original) The liquid crystal display panel as claimed in claim 9, wherein the dummy data line is supplied with a signal having an inverted phase with respect to data on a data line adjacent to the dummy data line.

11. (Original) The liquid crystal display panel as claimed in claim 9, further comprising:
a plurality of dummy switching devices positioned at intersections between the dummy data line and the gate lines; and
a plurality of dummy pixel electrodes each connected to one of the dummy switching devices.

12. (Original) The liquid crystal display panel as claimed in claim 9, wherein the dummy data line and the dummy pixel electrode each further includes a black matrix for blocking light.

13. (Original) The liquid crystal display panel as claimed in claim 10, further comprising: dummy voltage supply means for supplying the signal to the dummy data line.

14. (Original) The liquid crystal display panel as claimed in claim 13, wherein the dummy voltage supply means includes an inverter for performing a phase inversion of a signal on a data line adjacent to the dummy data line.

15. (Original) The liquid crystal display panel as claimed in claim 13, wherein the dummy voltage supply means includes a shorting line for electrically connecting the dummy data line to the data line supplied with a voltage having the same phase as a voltage applied to the dummy data line.

16. (Original) The liquid crystal display panel as claimed in claim 13, wherein the dummy voltage supply means includes a dummy voltage generator for directly applying a voltage to the dummy data line.

17. (Previously Presented) A method of driving a liquid crystal display panel having a plurality of pixels, comprising the step of:

supplying video signals to pixel electrodes in a display area;

supplying a signal to the dummy data line in a non-display area to allow each pixel to have a substantially same parasitic capacitance.

18. (Original) The method as claimed in claim 17, wherein the dummy data line is supplied with a signal having an inverted phase with respect to data on the data line being adjacent to the dummy data line.

19. (Previously Presented) A liquid crystal display (LCD) device, comprising:

a scanning line extending in a first direction;

a plurality of data lines extending in a second direction generally perpendicular to the scanning lines, such that the data lines cross the scanning lines, wherein the data lines and the scanning lines define a plurality of pixels;

a plurality of switching devices positioned where the data lines cross the scanning line, each switching device being connected with the scanning lines and one of the data lines;

a plurality of pixel electrodes each connected to one of the switching devices;

a dummy data line formed in parallel to the data lines to allow each pixel to have substantially a same parasitic capacitance.

20. (Original) The LCD device of claim 19, further comprising:
dummy voltage supply means for supplying a signal such that a first voltage charged into
a first one of the pixel electrodes which is adjacent to the dummy voltage line becomes
approximately the same as a second voltage charged into a second one of the pixel electrodes
disposed adjacent to the first one of the pixel electrodes.

21. (Original) The LCD device as claimed in claim 20, wherein the dummy voltage
supply means comprises an inverter for inverting a signal on a data line adjacent to the dummy
data line and supplying the inverted signal to the dummy data line.

22. (Original) The LCD device as claimed in claim 20, wherein the dummy voltage
supply means includes a shorting bar for electrically connecting the dummy data line to one of
the data lines which is not adjacent to the dummy data line.

23. (Original) The LCD device as claimed in claim 23, further comprising a column
driver for supplying data voltages to the data lines.

24. (Original) The LCD device as claimed in claim 23, wherein the dummy voltage
supply means includes a dummy voltage generator for directly applying a voltage to the dummy
data line.

25. (Original) The LCD device of claim 20, wherein adjacent data lines have data
signals with opposite polarities during a period when the scanning line supplies a scanning
signal to the switching devices.